

FIG. 1

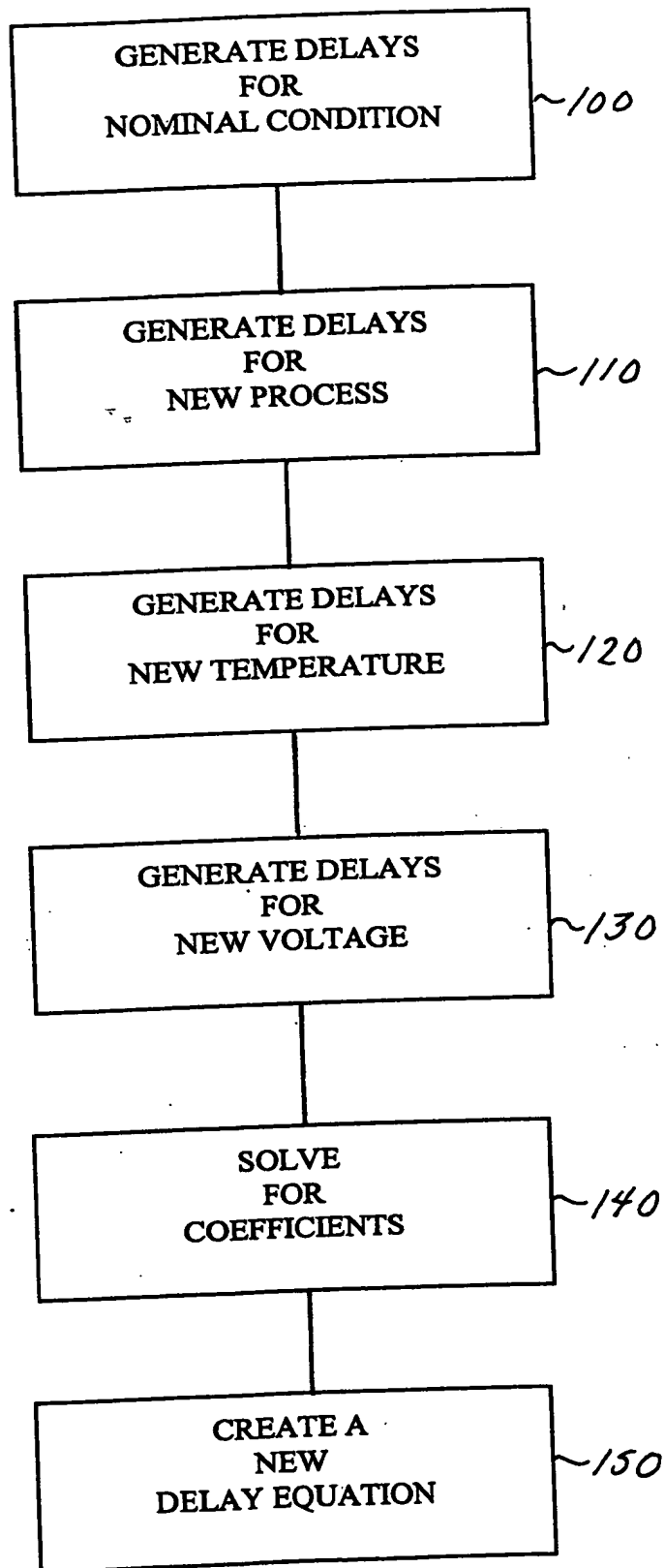


FIG. 2

FIG. 3 is a block diagram of a computer system 151. The system includes a microprocessor 152, a clock 170, a RAM 156, an expansion RAM 158, a ROM 154, an input/output (I/O) block 160, a display screen 162, an input device 184, a mass storage device 168, a removable storage device 188, and removable storage media 171. The system is connected to an expansion bus 166. The microprocessor 152 is connected to the clock 170, the RAM 156, the expansion RAM 158, the ROM 154, the I/O block 160, and the display screen 162. The RAM 156 is connected to the expansion bus 166. The expansion RAM 158 is connected to the RAM 156. The ROM 154 is connected to the expansion bus 166. The I/O block 160 is connected to the expansion bus 166, the input device 184, the mass storage device 168, the removable storage device 188, and the display screen 162. The mass storage device 168 is connected to the removable storage device 188. The removable storage device 188 is connected to the removable storage media 171.

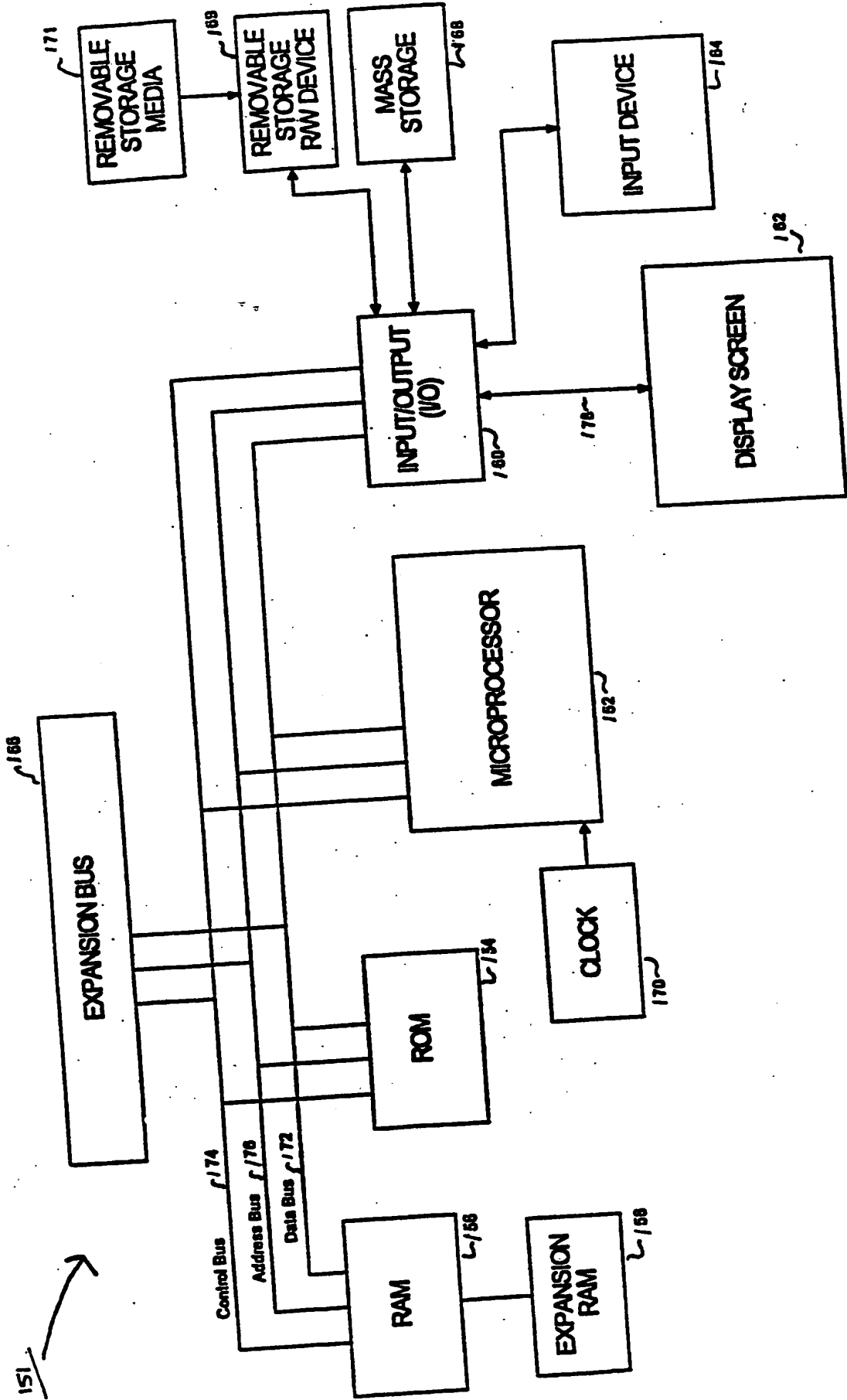


FIG. 3